

2187



Attorney Docket No. 42P11202

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Andy Glew et al.

Application No. 10/084,282

Filed: February 25, 2002

For: **METHOD AND APPARATUS  
FOR TRANSLATING GUEST  
PHYSICAL ADDRESSES IN A  
VIRTUAL MACHINE  
ENVIRONMENT**

Examiner: Bao Q. Truong

Art Unit: 2187

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Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.	
By: <u>Anne Collette</u> Anne Collette	Date: <u>4/22/04</u>

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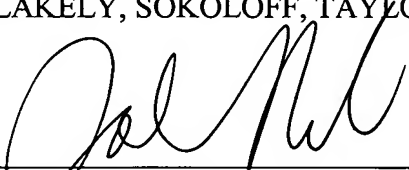
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 4/22/04

  
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Substitute for Form 1449-PTO (Modified)		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)		Application Number	10/084,282
		Filing Date	February 25, 2002
		First Named Inventor:	Andy Glew
		Group Art Unit	2187
		Examiner Name	Bao Q. Truong
Sheet 1 of 1	Attorney Docket Number	42P11202	Technology Center 2100

U.S. PATENT DOCUMENTS						
Exam. Initial*	Cite No. <sup>1</sup>	Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Code <sup>2</sup>	Kind (If known)			

FOREIGN PATENT DOCUMENTS								
Exam. Initial*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>8</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (If known)				
		EP	0 961 193	A2	Texas Instruments Inc.	12-01-1999		
		EP	0 965 902	A2	National Semiconductor Corp.	12-22-1999		
		EP	1 085 396	A1	Hewlett-Packard Company	03-21-2001		
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		WO	95/24696		Integrated Technologies of America, Inc.	09-14-1995		
		WO	99/18511		Phoenix Technologies, Ltd.	04-15-1999		
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		WO	00/21238		Intel Corporation	04-13-2000		
		WO	01/63994	A2	Iridian Technologies, Inc.	08-30-2001		

OTHER ART – NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published		T <sup>2</sup>
		INTEL CORPORATION, "IA-64 System Abstraction Layer Specification", Intel Product Specification, Order Number 245359-001, 112 pages, January 2000.		
		INTEL CORPORATION, "Intel IA-64 Architecture Software Developer's Manual – Volume 2: IA-64 System Architecture", Intel Product Manual, Order Number 245318-001, pp. i, ii, 5.1-5.3, 11.1-11.8, 11.23-11.26, January 2000.		

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